



Current Consumption of the A2100 module on EVA2100-A in Different Power Modes

Application Note

Version 1.2

Revision History

Rev.	Date	Description
1.0	04-18-11	First release
1.1	04-20-11	Minor corrections
1.2	05-06-11	1. Change the title 2. Add notes
	mm-dd-yy	

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1. Test Setup

Hardware: Maestro eva-board A2100, NI DAQ USB-6229

Active Antenna: INPAQ GPS05K-S3-04-A

Tools: Maestro GPS Power Test V1.00, LabVIEW

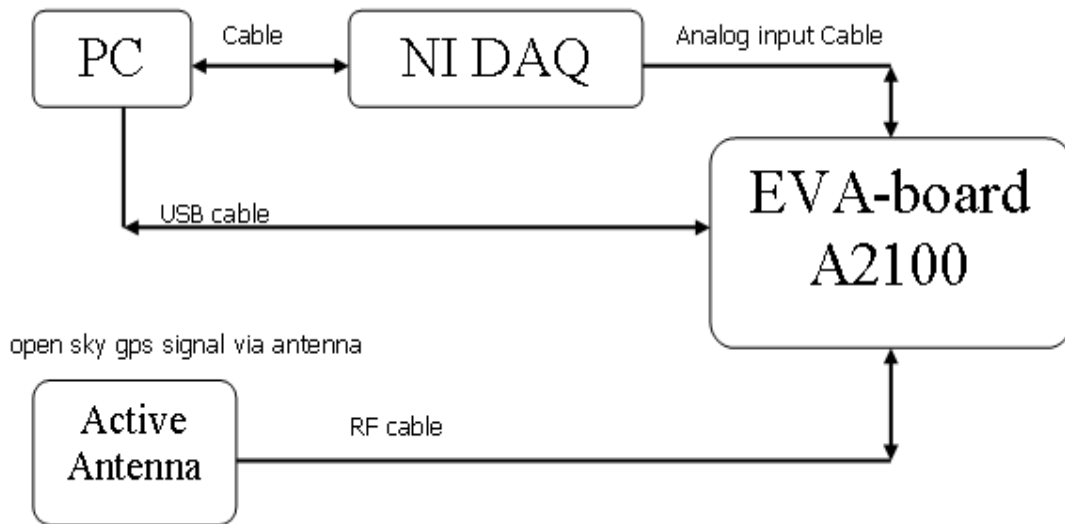


Fig. 1. Block Diagram

2. Full-power Mode

VCC	State	Icc (3) (Average)
3.30V ± 50mV	Search ₍₁₎	35mA
3.30V ± 50mV	Tracking ₍₂₎	24 mA

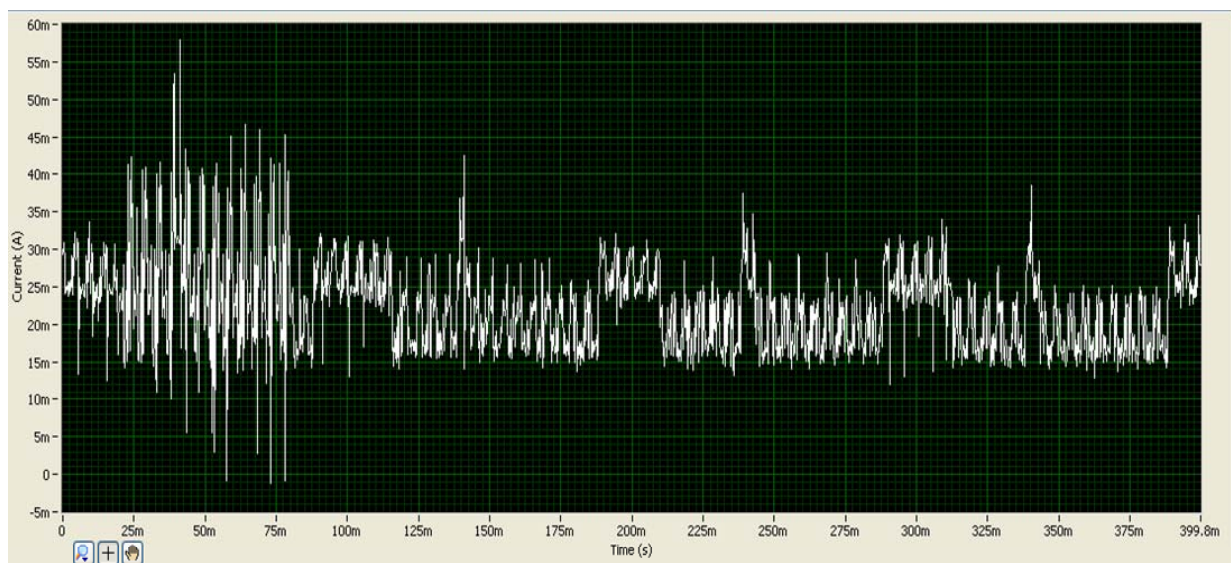


Fig. 2. Full-power Mode

3. Push-to-Fix Mode

Common settings

- Push to Fix Period : 200S
- Max Search Time : 120000ms
- Max off Time : 30000ms

VCC	Icc ₍₃₎ (Average)
3.30V ± 50mV	0.7mA

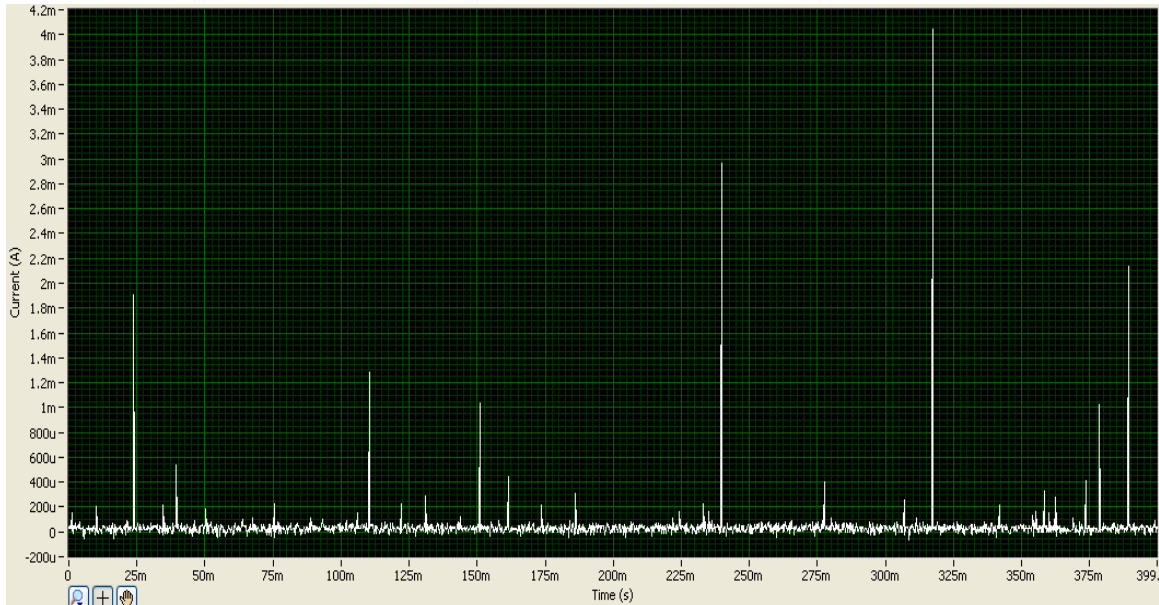


Fig. 3. Push-to-Fix Mode

4. Micro Power Mode (SiRFaware)

VCC	Icc ₍₃₎ (Average)
3.30V ± 50mV	40uA

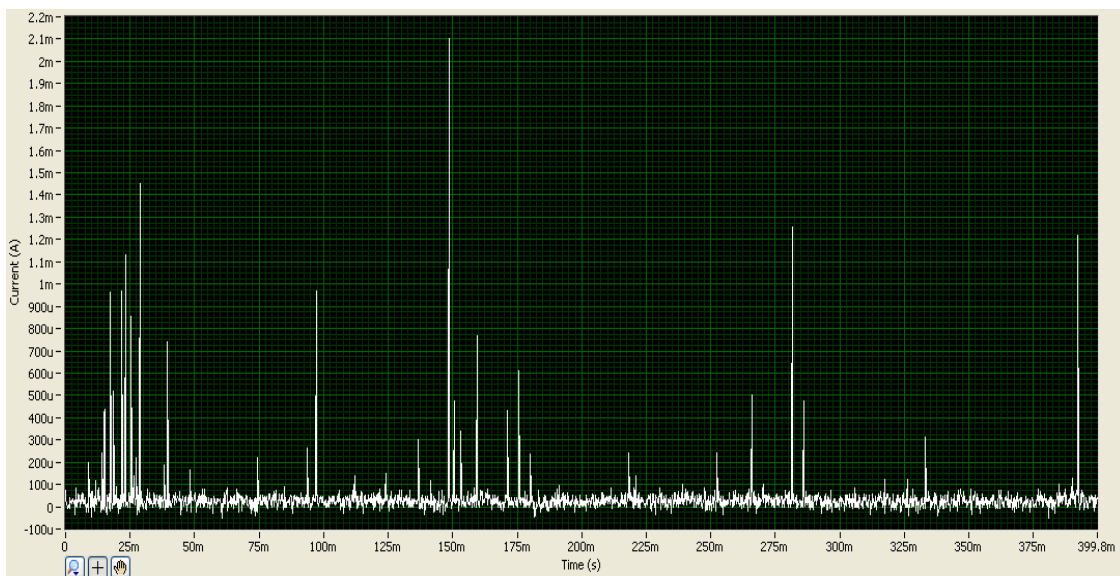


Fig. 4. Micro Power Mode (SiRFaware)

5. Hibernate Status

VCC	Icc (3) (min)	Icc (3) (typical)	Icc (3) (max)
3.30V ± 50mV	TBD	23.5uA	TBD

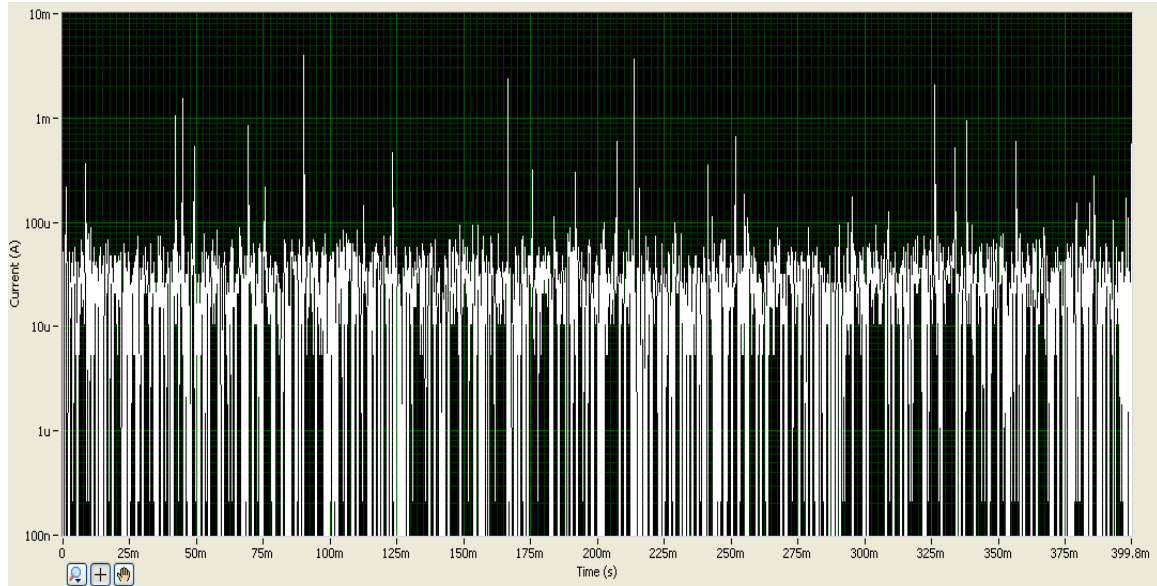


Fig. 5. Hibernate Status

- (1) Before location fix
- (2) After location fix and maintain in navigation
- (3) Only the A2100-A module power consumption

6 Related Information

6.1 Contact

This manual was created with due diligence. We hope that it will be helpful to the user to get the most out of the GPS module.

Anyway, inputs about errors or mistakable verbalizations and comments or proposals to Maestro, HongKong, for further improvements are highly appreciated.

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4.2 Related Documents

- GPS Receiver A2100 (Maestro)